

501.42810X00

March 27, 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

T. HONMURA

Application No.:

10/603,797

Filed:

June 26, 2003

For:

SEMICONDUCTOR INTEGRATED CIRCUITS

Art Unit:

2182

Examiner:

T. NGUYEN

<u>AMENDMENT</u>

MS: AMENDMENTS
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated October 25, 2005, the period of response for which extension of time is requested in the attached Petition for Extension of Time, please amend the above-identified application as listed below and as set forth on the following pages:

Amendments to the Claims;

Remarks are included following the amendments.